



EXPRESS MAIL NO. EL872037792US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Reissue Patent Application:

Applicant : Leland R. Nevill
Assignee: : Micron Technology, Inc.
Filed : November 16, 2001
For : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS
Docket No. : 500060.02

Corresponding Issued U.S. Patent:

Patent No. : 5,984,190
Issued : November 16, 1999
Application No. : 08/857,100
Filing Date : May 15, 1997
Examiner : Daniel St. Cyr
Art Unit : 2876

BOX REISSUE
Commissioner for Patents
Washington, DC 20231

REISSUE DECLARATION

As below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below.

I believe that I am the original, first and sole inventor of the innovative subject matter described and claimed in the application for reissue of U.S. Patent No. 5,984,190, which is entitled "METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS."

I hereby state that I have reviewed and understand the contents of the above-identified U.S. reissue patent application, INCLUDING BOTH THE ORIGINAL CLAIMS AND THE NEW CLAIMS PRESENTED IN THE ENCLOSED PRELIMINARY AMENDMENT.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulation, § 1.56(a).

I believe the above-identified original patent 5,984,190 to be partly inoperative, by reason of the patentees claiming less than we had a right to claim. For example, claim 1 of the '190 patent is limited to using a lookup table to associate the optical identification code with the corresponding electronic identification information, but applicant has a right to claim that other means may be used to associate the optical identification code with the corresponding electronic identification information. This error arose without any deceptive intention on my part.

Accordingly, the enclosed Preliminary Amendment presents additional independent apparatus claims that are the error noted above. These independent claims have been newly drafted to more particularly point out and distinctly claim the invention. The Preliminary Amendment also presents additional dependent claims and some method claims. I believe that my invention is at least as broad as these claims.

I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof.

I do not know and do not believe that the claimed invention was ever patented or described in any printed publication in any country before my invention thereof.

I do not know and do not believe that the claimed invention was ever patented or described in any printed publication in any country more than one year prior to the filing date of the original U.S. application.

I do not know and do not believe that the claimed invention was ever in public use or on sale in the United States of America more than one year prior to the filing date of the original U.S. application.

I hereby declare that all statements made of my knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and may also jeopardize the validity of the application or any patent issued thereon.

Inventor: Leland R. Nevill

Date

Residence and Mailing Address: 6803 Diamond Street, Boise, Idaho 83709

Citizenship: United States of America



EXPRESS MAIL NO. EL872037792US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Reissue Patent Application:

Applicant : Leland R. Nevill
Assignee: : Micron Technology, Inc.
Filed : November 16, 2001
For : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED
CIRCUITS
Docket No. : 500060.02

Corresponding Issued U.S. Patent:

Patent No. : 5,984,190
Issued : November 16, 1999
Application No. : 08/857,100
Filing Date : May 15, 1997
Examiner : Daniel St. Cyr
Art Unit : 2876

BOX REISSUE
Commissioner for Patents
Washington, DC 20231

OFFER TO SURRENDER THE ORIGINAL PATENT

Sir:

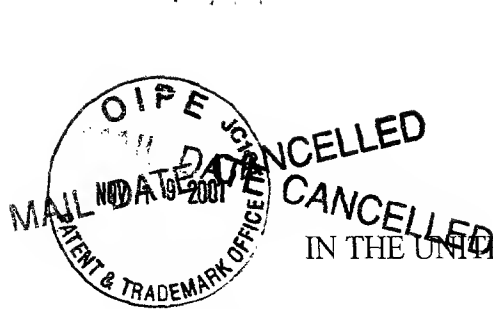
Michael L. Lynch, Chief Patent Counsel of the Assignee of the entire interest in U.S. Letters Patent No. 5,984,190 which is entitled METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS and issued on November 16, 1999 to Micron Technology, Inc., hereby offers to surrender said Letters Patent.

Micron Technology, Inc.

Date

By:

Michael L. Lynch
Chief Patent Counsel



EXPRESS MAIL NO. EL872037792US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Reissue Patent Application:

Applicant : Leland R. Nevill
Assignee: : Micron Technology, Inc.
Filed : November 16, 2001
For : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED
CIRCUITS
Docket No. : 500060.02

Corresponding Issued U.S. Patent:

Patent No. : 5,984,190
Issued : November 16, 1999
Application No. : 08/857,100
Filing Date : May 15, 1997
Examiner : Daniel St. Cyr
Art Unit : 2876

BOX REISSUE
Commissioner for Patents
Washington, DC 20231

ASSENT OF ASSIGNEE

Sir:

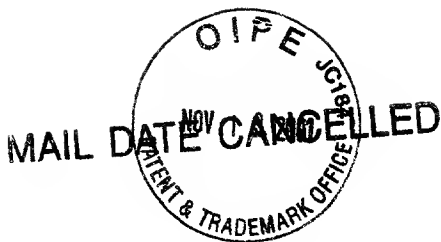
The undersigned, Micron Technology, Inc. (hereinafter "Micron"), a corporation of the State of Delaware having a place of business at 8000 South Federal Way, Boise, Idaho 83716, declares that it is the assignee of the entire right, title, and interest in the above-identified U.S. Patent No. 5,984,190. Documentary evidence of chain of title from the original owner to Micron has been filed with and recorded by the United States Patent Office at Reel 8567, Frame 0039. The evidentiary documents referred to in the instant Assent of Assignee have been reviewed by the undersigned and it is certified that, to the best of Micron's knowledge and belief, title is in Micron.

The undersigned Assignee hereby assents to the reissue application.

Micron Technology, Inc.

By: _____
Michael L. Lynch
Chief Patent Counsel

Date



EXPRESS MAIL NO. EL872037792US

CERTIFICATE UNDER 37 C.F.R. § 3.73(b)

Applicant : Leland R. Nevill
Patent No. : 5,984,190
Issued : November 16, 1999
Filed : November 16, 2001
For : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS

MICRON TECHNOLOGY, INC.,
(Name of Assignee)

a Corporation
(Type of Assignee, e.g., corporation,
partnership, government agency, etc.)

certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of either:

- A. ☒ An assignment from inventors of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel 8567 Frame 0039 (copy enclosed).
- B. ☐ A chain of title from the inventor(s), of the patent application identified above, to the current assignees as shown below:
1. From: _____ To: _____
A copy is attached.
 2. From: _____ To: _____
A copy is attached.
 3. From: _____ To: _____
The document was recorded in the Patent and Trademark Office at Reel _____
Frame _____ or for which a copy thereof is attached.
- ☐ Additional documents in the chain of title are listed on a supplemental sheet.
- ☐ Copies of assignments or other documents in the chain of title are attached.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

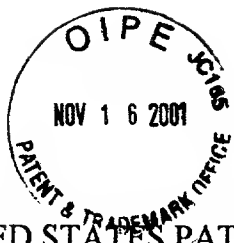
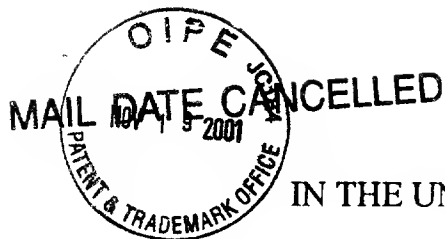
I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: _____

Name: Michael L. Lynch

Title: Chief Patent Counsel

Signature: _____



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Leland R. Nevill

Serial No.:

Filed: Concurrently herewith

For: METHOD AND APPARATUS
FOR IDENTIFYING
INTEGRATED CIRCUITS

Docket No.: 660073.555

Disclosure Number: 95-1008

Group Art Unit:

Examiner:

ASSIGNMENT:

☒ Enclosed for recording☐ Previously recorded

Date: _____

Reel: _____

ASSIGNMENT

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned does hereby:

SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, P.O. Box 6, Boise, Idaho 83707-0006, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled "**METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS**"; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

